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# The asymmetric band structure and electrical behavior of the GdScO<sub>3</sub>/GaN system

S. lacopetti, <sup>1,a)</sup> P. Shekhter, <sup>1</sup> R. Winter, <sup>1</sup> T. C. U. Tromm, <sup>2</sup> J. Schubert, <sup>2</sup> and M. Eizenberg <sup>1</sup> Department of Materials Science and Engineering, Technion-Israel Institute of Technology, Haifa, Israel <sup>2</sup> Peter Grünberg Institute 9, Forschungszentrum Jülich GmbH, Jülich, Germany

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III—V nitrides are interesting materials for a very wide variety of electronic and optoelectronic devices. In this study, their interaction with GdScO<sub>3</sub> (GSO), a ternary rare earth oxide, is investigated for MOS applications. We compare pulsed laser deposited amorphous and crystalline epitaxial GdScO<sub>3</sub> in terms of their band alignment with the underlying GaN substrate and the resulting electrical characteristics of the MOS stack. The crystal structure of GdScO<sub>3</sub> and GaN is investigated by means of x-ray diffraction, showing that crystalline oxide is growing epitaxially on GaN. X-ray photoelectron spectroscopy analysis shows a staggered band alignment with a GdScO<sub>3</sub>-GaN valence band offset of 3.6–3.7 eV, which is reflected in a very asymmetric current-voltage behaviour of the MOS capacitors: breakdown at positive bias, significantly earlier for the crystalline oxide (around 5 MV/cm) compared to the amorphous oxide (around 8 MV/cm), and no breakdown up to a field of -14 MV/cm at negative bias. Transmission electron microscopy images show a crystalline, two-atom thick interface layer between GaN and both crystalline and amorphous GdScO<sub>3</sub>, which is thought to be an electron barrier between GSO and GaN and a possible source of the staggered band alignment. The electrical behaviour can be exploited for asymmetric nano-electronic devices. *Published by AIP Publishing*. [http://dx.doi.org/10.1063/1.4983559]

#### INTRODUCTION

III-V nitrides are characterized by wide, direct band gap and high electron mobility hence, they have been, in recent times, extensively studied for optoelectronic as well as power devices. 1,2 These semiconductors found a major application in the light emitting diode (LED) industry, where GaN is used to produce white LEDs, while the research is looking towards UV and deep UV LEDs.<sup>3</sup> Due to their high and composition-tuneable band gap, 4 thermal and chemical stability, and high mobility aforementioned, III-V nitrides have also been studied for metal-oxidesemiconductor field effect transistors (MOSFETs), high electron mobility transistors (HEMTs), radio frequency (RF) and microwave devices, sensors, and solar cells, especially for space application where resistance to hard radiation is necessary.<sup>5,6</sup> One important semiconductor in the III-V nitride family is GaN that shows a direct band gap of 3.4 eV and can be grown on Si(111) wafers by molecular beam epitaxy (MBE), making it an interesting substitute for Si- and GaAs-based devices. 1

High-k oxides have been extensively studied in order to achieve high gate oxide capacitance without decreasing the oxide layer thickness, which would increase the formation of leakage currents. The main requirements for a high-k gate oxide are chemical and structural compatibility with the semiconductor and a wide band gap. Amorphous HfO<sub>2</sub> is already being used as a high-k dielectric deposited on Si-based devices, but it still shows unwanted recrystallization and high diffusivity of oxygen, which induces non-reproducible modification

In this study, we focused on the electrical and structural characterization of MOS capacitors based on GaN as a semiconductor and GdScO<sub>3</sub> (GSO) as a high-k dielectric, as the latter shows a dielectric constant of 24 in the stable hexagonal phase, as discovered in previous studies. <sup>12</sup> Single crystalline and amorphous GSO were deposited on GaN aiming at a comparison of electrical characteristics and phase stability between single crystalline and amorphous layers. The differences in band alignment and electrical behaviour were investigated for both the amorphous and crystalline oxides deposited on GaN.

## **EXPERIMENTAL**

A 1  $\mu$ m thick n-GaN (Si-doped, n = 3 × 10<sup>18</sup> cm<sup>-3</sup>) layer was grown by MBE on top of Si(111) with the assistance of buffer layers to minimize the dislocations in the GaN. Crystalline and amorphous GSO were subsequently grown by pulsed laser deposition (PLD). A KrF excimer laser ( $\lambda$  = 248 nm) was used to deposit GSO in an O<sub>2</sub> ambient (p<sub>O2</sub> > 10<sup>-3</sup> mbar), with a pulse width of 20 ns and a fluence

of electrical characteristics of the final device; stable single crystals do not recrystallize during processing as well as do not induce surface defects in the semiconductor, and therefore, they are suitable candidates for a reliable gate oxide, as indicated by the ITRS international roadmap of semiconductors. Rare-earth oxides are characterized by a large dielectric constant combined with a relatively wide band gap and chemical stability. Studies on ternary rare-earth oxides showed stable crystallinity and very low oxygen diffusivity, making them interesting candidates for better performances of the dielectric. 11

a)iasara@tx.technion.ac.il

of around 5 J/cm<sup>2</sup> at a repetition rate of 10 Hz. A preheat temperature of 700 °C was applied for the crystalline GSO deposition, while room temperature was used for the deposition of the amorphous oxide. Two different thicknesses of the oxide, 4 nm and 30 nm, were chosen according to the required analysis method. A bare GaN sample was used for structural and band alignment characterization.

Al contacts for MOS capacitors were deposited using an e-gun with a thickness of  $100 \, \text{nm}$ . The substrate (back) contact for each sample was obtained by reactive ion etching (RIE) through the oxide layer using a mixture of CF<sub>4</sub> and O<sub>2</sub> at P < 5 mTorr, room temperature, and a RF power of  $175 \, \text{W}$ , followed by  $100 \, \text{nm}$  thick Al deposition using an e-gun on the exposed GaN.

X-ray diffraction (XRD) and X-ray reflectivity (XRR) measurements were performed in a Rigaku SmartLab x-ray diffractometer equipped with a Cu K $\alpha$  source and a rotating anode, making use of a Ge(220) two-bounce monochromator. GaN is a wurtzite phase, and on top of it, an epitaxial hexagonal oxide layer was grown, and therefore,  $\theta$ -2 $\theta$  spectra were acquired with tilting angles of 0° [corresponding to the (002) plane] and 61.92° [corresponding to the (101) plane of GSO]; d-spacings are proportional to c and a as previously derived in Ref. 13. The tilting angle was defined by the measured c lattice constant of GSO and the initial hypothesis of a/c having approximately the value of a/c measured in GaN; repetitions of the experiment for a few angles in the interval of  $\pm$ 1° around the central value were carried out to remain in agreement with the highest intensity of the signal.

X-ray photoelectron spectroscopy (XPS) analyses were carried out on the 4 nm thick film and bare samples in order to get signals from both GSO and GaN underneath, hence giving the possibility to determine the band alignments of crystalline and amorphous GSO-GaN. A Thermo VG Scientific Sigma Probe with a monochromatic Al K $\alpha$  x-ray source (h $\nu$  = 1486.6 eV) was used for the XPS experiments. Ga3p and OIs peaks were acquired with a pass energy of 50 eV and fitted by applying Shirley background and Lorentzian to a Gaussian peak ratio of 15%, using the XPSPeak 4.1 software. Charge removal was done by comparison of the Ga3p peak with the values of the same peaks in GaN standards from the literature,  $^{14-16}$  aligning the Ga $3p^{3/2}$  peak at 105.1 eV. The valence band (VB) and electron energy loss (EELS) spectra of the OIs peak have also been acquired with the same resolution of 0.05 eV.

Current-voltage (I-V) measurements of MOS capacitors based on the 30 nm thick oxides were carried out using an Agilent 4155C tool. A sweep DC voltage was applied in negative bias up to the limit of the instrument (0, -40 V) and in positive bias up to the breakdown (BD) voltage of the sample. Contact areas were measured using an optical microscope. Transmission electron microscopy (TEM) Bright Field images were acquired using a Tecnai G<sup>2</sup> T20 S-Twin TEM.

### **RESULTS AND DISCUSSION**

The  $\theta$ -2 $\theta$  spectra of the crystalline 30 nm thick GSO film, amorphous 30 nm thick GSO film, and bare GaN sample are presented in Fig. 1. It can be observed that, in addition to the

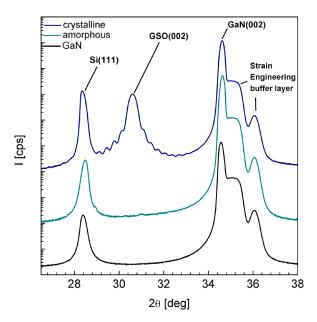


FIG. 1.  $\theta$ -2 $\theta$  spectra of crystalline GSO-GaN, amorphous GSO-GaN, and bare GaN.

Si(111) reflection, two peaks at  $2\theta = 30.62^{\circ}$  and  $2\theta = 34.60^{\circ}$  are present in the crystalline spectrum. The bare GaN and the amorphous GSO-GaN show only the GaN(002) reflection at  $2\theta = 34.60^{\circ}$ , hence confirming that the first oxide is single crystalline and the second is an amorphous phase. The lattice parameter of the cell is  $c_{GaN} = 5.2 \,\text{Å}$  for GaN in all the cases and  $c_{ox} = 5.8 \,\text{Å}$  for GSO in the case of the crystalline oxide. A shoulder of the GaN peak at higher angles is visible in all the spectra: its multi-peak shape is compatible with the strain engineering buffer layer deposited on Si(111) before the deposition of GaN by MBE, as mentioned in the fabrication procedure.

A  $\theta$ -2 $\theta$  scan with a tilt angle of  $\chi = 61.37^{\circ}$  on the crystalline 30 nm GSO film exhibits a peak from the (101) plane of GSO at  $2\theta = 31.98^{\circ}$  while GaN produced a peak at  $2\theta = 36.49^{\circ}$ , associated with the values of  $a_{ox} = 3.7$  Å and  $a_{GaN} = 3.2$  Å (Fig. 2), corresponding to a lattice mismatch of

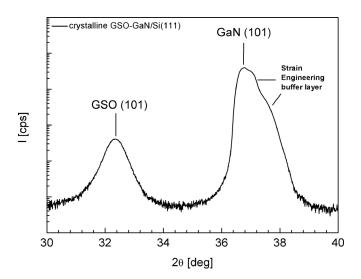


FIG. 2. Tilted  $\theta$ - $2\theta$  spectrum of crystalline GSO-GaN/Si, showing reflection of the (101) plane from GaN and GSO.

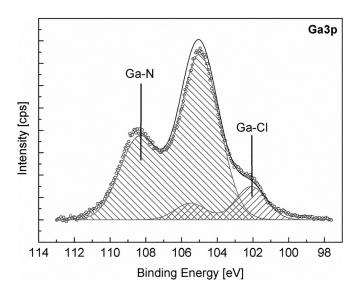


FIG. 3. Ga3p peak of the bare GaN substrate, showing Cl contamination and no native oxide formation.

 $\sim$ 14%, coherent with the values found in the literature. <sup>12</sup> Therefore, a hexagonal relaxed epitaxial GSO layer has been successfully grown on GaN (wurtzite).

The exact thickness of these oxides was verified by the XRR technique (spectra not shown), yielding values of 27.9 nm and 28.0 nm for the crystalline oxide and the amorphous, respectively.

XPS analysis shows chlorine contamination in GaN (Fig. 3): this is due to commonly used cleaning procedures. <sup>17</sup> Band alignment was determined by finding, via XPS measurements, the  $E_{\rm gap}$  of GSO and the valence band offset (VBO) of GaN with respect to GSO. <sup>18</sup>  $E_{\rm gap}=3.4\,{\rm eV}$  for wurtzite GaN was used. <sup>4</sup> Electron energy loss spectroscopy (EELS) of the O*Is* peak was used to evaluate  $E_{\rm gap}$  of GSO (Fig. 4). The values found in this way are  $5.3\pm0.1\,{\rm eV}$  and  $5.4\pm0.1\,{\rm eV}$  for the crystalline and amorphous GSO, respectively, in agreement with the values found in the literature. <sup>11,12</sup> In the

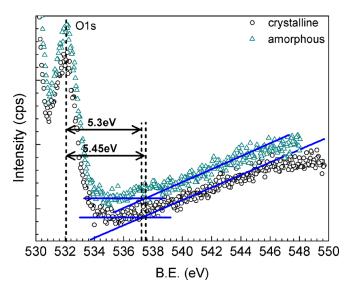


FIG. 4. EELS and energy gap of crystalline GSO-GaN and amorphous GSO-GaN. In blue, peak linear regression and background average are higlighted.

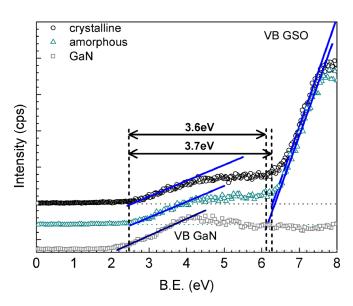


FIG. 5. VBO calculated from the VB spectra of crystalline GSO-GaN, amorphous GSO-GaN, and bare GaN. In blue, VB linear regressions are highlighted.

systems under analysis, electrons are emitted from both GSO and GaN since the oxide film thickness is lower than the escape depth of the electrons in the operational conditions, namely 3 times the incident wavelength. Hence, the two onsets of VB are visible at low binding energies (Fig. 5). The distinction between the VB of GSO and GaN has been deduced from the analysis of the bare GaN sample. The VBO of GaN with respect to that of GSO is calculated from the contributions in the VB region in the XPS spectra (Fig. 5), and its values are  $3.7 \pm 0.1 \, \text{eV}$  and  $3.6 \pm 0.1 \, \text{eV}$  for the crystalline and the amorphous oxides, respectively. The band alignment of the two systems is summarised in Fig. 6.

It can be seen that the band alignment of the two systems is practically identical with a negative conduction band offset (CBO) of 1.6–1.8 eV between GaN and GSO, which means that the band alignment is staggered, an unlikely situation when high- $\kappa$  oxides are deposited on top of semiconductors, according to what is found in the literature. <sup>11,19</sup>

The staggered band alignment is expected to have a strong influence on the electrical behaviour of the Al/GSO/GaN MOS capacitors since no potential barrier exists for electrons in GaN when applying a positive bias to the gate

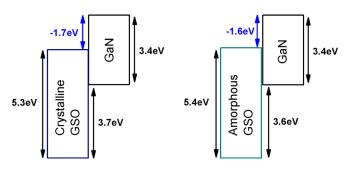


FIG. 6. Band alignment of both amorphous and crystalline GSO on GaN. In blue, the negative conduction band offset is highlighted.

electrode. At negative bias, a large barrier for holes exists, as well as a very high barrier for electrons. This hinders any significant current up to higher fields than in the case of the straddling band alignment which would give an asymmetry for n-doping only. The current-voltage (IV) behaviour was analysed (Fig. 7) in light of the asymmetric band alignment. JE curves are plotted to show current values normalised to the capacitors' area and applied voltage values normalised to the film thickness. The breakdown (BD) for the crystalline GSO film with applied positive bias is always detected around fields of ( $+6 \pm 1.5$ ) MV/cm, while the breakdown has never been observed at fields less negative than -14 MV/cm when applying negative bias. In positive bias, a gradual increase in current is noticeable prior to BD.

Al/amorphous GSO/GaN capacitors produce breakdown in forward bias at statistically higher electric fields than the crystalline GSO/GaN in the same conditions, between 8 and 9 MV/cm. The same behaviour of the crystalline GSO was observed when applying negative bias. The asymmetric behaviour is clearly marked, coherently with the staggered band alignment found via XPS analysis of the 4 nm film sample; nonetheless, the breakdown at positive bias is not immediate. The higher BD voltage in the case of the amorphous oxide can be inferred by the higher resistance of the amorphous film with respect to the crystalline film or by the higher defectiveness of the crystalline film induced by defects of the substrate on which it was epitaxially grown. It can be hypothesised that GaN dislocations, that cause large pinholes in GaN, propagate into the crystalline GSO layer and create paths of least resistance that give way to earlier breakdown. The amorphous layer, however, is not sensitive to GaN defects as it does not reproduce the crystallinity of the substrate. In addition to these considerations, the hypothesis of the presence of an interfacial electron barrier not detected by XPS should be strongly considered.

TEM images of crystalline and amorphous GSO-GaN interfaces are given in Figs. 8 and 9, respectively. An

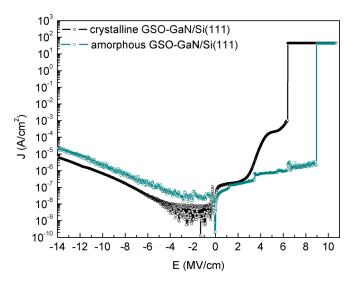


FIG. 7. JE plot of crystalline GSO-GaN and amorphous GSO-GaN. Asymmetric positive-negative electrical behaviour observed due to BD voltage in positive bias.



FIG. 8. TEM image of crystalline GSO-GaN showing the nature of the interface layer.

interface layer can be noticed between GaN and GSO. In bright field TEM, it appears with a lighter contrast, indicating a lower density. Unlike spontaneous interfacial layers, the layer observed appears to have a crystalline nature, pointing to the fact that it is not a native oxide but rather a part of the deposited film. This observation, combined with the absence of Ga-O bonds in the Ga3p spectrum reported in Fig. 3 and no N bonds other than N-Ga detected in the N1s peak (not shown), leaves the possibility of a Sc<sub>2</sub>O<sub>3</sub> segregation at the interface occurring during or after the PLD deposition. According to the literature, binary rare earth oxides such as Sc<sub>2</sub>O<sub>3</sub> and La<sub>2</sub>O<sub>3</sub> show a positive conduction band offset. 11,19 It can be hypothesised that the so-called Sc-rich layer constitutes a thin electron barrier between the semiconductor and the dielectric, representing the source of the structure's band alignment and asymmetric electrical behaviour with non-immediate breakdown at positive bias. Thereby, further studies on the full structure's band alignment are currently carried out.

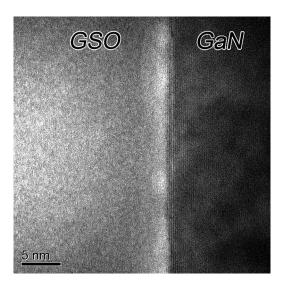


FIG. 9. TEM image of amorphous GSO-GaN showing the crystalline interface layer.

#### **SUMMARY**

The system of GSO deposited on top of GaN was characterised structurally and electrically. XRD analyses show that single crystalline GSO is successfully grown as a hexagonal layer on top of wurtzite GaN, with a lattice bigger by 14%. Amorphous GSO is successfully obtained, without any GSO crystallization taking place. The band alignment of GSO and GaN was found to be staggered, with no conduction band barrier for both the crystalline and amorphous oxides. Due to the absence of a barrier for electrons, the electrical behaviour of these systems became extremely asymmetric with different current characteristics on either bias. BD was recorded only at positive bias with an average field of around 5 MV/cm for the epitaxial oxide, while fields of up to 14 MV/cm did not cause any BD on the negative side. It was also seen that the amorphous GSO film is more resilient against BD than its crystalline counterpart. A thin crystalline Sc<sub>2</sub>O<sub>3</sub> segregation layer was visible at both the crystalline and amorphous GSO-GaN interfaces, and it is believed to be a thin electron barrier between GaN and GSO, responsible for the non-immediate BD at positive bias. The asymmetric electrical behaviour can be utilised to fabricate asymmetric nano-electronic devices, while the staggered band alignment results in a disadvantage for the high- $\kappa$  oxide application in nitride-based MOS devices, original intention of the investigation. However, the presence of a Sc-rich electron barrier between GSO and GaN, if confirmed in further experiments, will allow the application of these systems in GaN-based MOS devices, with room for investigation on the nature and role of the barrier layer as a function of production parameters and treatments.

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